

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10672961			
<b>Filing Date:</b>	26-Sep-2003			
<b>Title of Invention:</b>	Three dimensional multi layer memory and control logic integrated circuit structure			
<b>First Named Inventor/Applicant Name:</b>	Glenn J. Leedy			
<b>Filer:</b>	Michael James Ure			
<b>Attorney Docket Number:</b>	ELM-2 CONT. 4			
Filed as Small Entity				
<b>Utility under 35 USC 111(a) Filing Fees</b>				
<b>Description</b>	<b>Fee Code</b>	<b>Quantity</b>	<b>Amount</b>	<b>Sub-Total in USD(\$)</b>
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Claims in excess of 20	2202	6	26	156
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
<b>Post-Allowance-and-Post-Issuance:</b>				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>156</b>